A BATCH VERIFYING AND DETECTING THE ILLEGAL SIGNATURES

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Abstract. The concept of batch verifying multiple RSA digital signatures is to find a method that multiple digital signatures can be verified simultaneously in only one exponential operation time. In this article, we proposed a new batch verifying multiple RSA digital signatures scheme. The main contribution of the proposed scheme is that it can easily discover where the signature-verification fault is located without re-verifying all individual signatures separately.

Keywords: Digital signature, information security, multiple signatures, PKI, RSA.

1. Introduction. In 1978, Rivest, Shamir, and Adleman proposed a famous asymmetric cryptosystem named RSA [26] which included four main characteristics: user authentication, confidentiality, integrity, and non-repudiation. It can protect the transaction information which can be safely transmitted and avoid the problems of tampering or usurped the information over the network [12, 13, 14, 15]. In addition, it also solved the requirement of user authentication and communication security on networking environments [16, 17, 18, 19, 20, 21, 23, 24].

RSA utilized two different keys to perform encryption and decryption, the public key (e) and the private key (d), respectively. In RSA signature mechanism, both the signer and receiver have the private key and public key of itself own [6, 7, 11, 16, 17, 27, 28, 29]. First of all, the signer used personal private key to sign documents $M_i$ (where $i = 1$ to $t$) and generated $t$ signatures when the signing process is completed. Then, the signer transmitted $t$ documents and signatures $S_i$ (where $i = 1$ to $t$ and $S_i = M_i^d$) to receiver.

After receiving these documents and signatures, the receiver used signer’s public key to verify each of these $t$ signatures one by one and checks whether $M_i = S_i^e$ holds or not. During authentication and verification phase, it will reduce the computer host’s processing ability because it needs to consume a large amount of exponential computation time. Therefore, the concept of batch verifying multiple RSA

\footnote{Responsible for correspondence: Prof. Min-Shiang Hwang.}
2. Analysis of Harn’s Scheme. Before reviewing Harn’s scheme, we reviewed the RSA digital signature scheme first of all. There are two prime numbers, \( p \) and \( q \) generated and a modulus \( N \) which is obtained by multiplying \( p \) and \( q \) together. In addition, \( e \) and \( d \) are represented as the signer’s public key and private key, respectively. If signer Alice wants to transmit the message \( M \) to receiver Bob, she must generate a digital signature \( S \) by using the formula \( S = h(M)^d \mod N \), where \( h(\cdot) \) represents a public one-way hashing function. Next, when Bob receiving \((M, S)\) from Alice, he verifies the signature \( S \) by using the formula \( h(M) = S^e \mod N \). As a result, suppose there are \( t \) documents and \( t \) digital signatures, it needs \( t \) times signing process and verification process for signer and verifier, respectively. Therefore, it is inefficient if there are multiple digital signatures and Harn proposed an efficient batch verifying multiple RSA digital signatures scheme \([5]\) which can be verified simultaneously in only one exponential operation time.

In Harn’s scheme, suppose Alice wants to transmit the messages \( M_1, M_2, \ldots, M_t \) and signatures \( S_1, S_2, \ldots, S_t \) to Bob, all of these signatures \( S_i = h(M_i)^d \mod N \), \((i = 1, 2, \ldots, t)\) would be generated by using Alice’s private key \( d \). Then, Bob uses Alice’s public key \( e \) to verify messages \( M_1, M_2, \ldots, M_t \) and its corresponding signatures \( S_1, S_2, \ldots, S_t \) by using the formula shown as follows:

\[
(\prod_{i=1}^{t} S_i)^e = \prod_{i=1}^{t} h(M_i) \mod N. \tag{1}
\]

If Equation (1) holds, it means signatures \( S_1, S_2, \ldots, S_t \) are valid generated from Alice’s messages \( M_1, M_2, \ldots, M_t \). Furthermore, in Equation (1), these multiple signatures can be verified simultaneously in one exponential operation time.

2.1. The Advantage of Harn’s Scheme. In conventional PKI mechanism, the signer signs messages \( M_1, M_2, \ldots, M_t \) with personal private key and transmits corresponding messages \( M_1, M_2, \ldots, M_t \) and signatures \( S_1, S_2, \ldots, S_t \) to the receiver one at a time. Then the receiver verifies the digital signatures with the signer’s public key one by one. However, in Harn’s scheme, it can be verified in only one exponential operation time and the proposed scheme is efficient for batch signature verification.

2.2. The Weakness of Harn’s Scheme. At present, there are two weaknesses in Harn’s scheme. In 2000, Hwang et al. showed that the Harn’s scheme could not find out two kinds of attacks. In the first attack, we exchanged two messages and signatures at random, Harn’s scheme was unable to detect the illegal signatures. For example, we assume the signer Alice sends messages and signatures to the receiver Bob. The messages and signatures are generated as follows: \( S'_i = h(M_{f(i)})^d \mod N \). \( S'_i \) means a false signature which is generated from signature \( S_i \) and \( f(\cdot) \) is a one-to-one mapping function (i.e., \( f(i) = j, \ldots, j \)).
i = 1, 2, · · · , t, j = 1, 2, · · · , t). Owing to the equation $(\prod_{i=1}^{t} S_i)^{e} = \prod_{i=1}^{t} h(M_{f(i)}) \mod N$, Alice could easily make the false signature and Bob will believe the signature is legally generated from Alice. The following is an example to illustrate the first attack in brief.

We assume Alice is a dishonest signer and sends three messages $(M_1, S_1')$, $(M_2, S_2')$, and $(M_3, S_3')$ to receiver Bob. First, Alice could forge these three signatures $S_1' = h(M_2)^a \mod N$, $S_2' = h(M_3)^a \mod N$ and $S_3' = h(M_1)^a \mod N$, respectively. Then, Bob will verify these fake signatures $M_1$, $M_2$ and $M_3$ by the equation $(\prod_{i=1}^{t} S_i)^{e} = \prod_{i=1}^{t} h(M_{f(i)}) \mod N$ which was introduced previously. Consequently, Bob still makes the batch verification valid because of the commutative principle of multiplication. But actually, Alice could claim that she didn’t transmit the messages to Bob by this equation, $h(M_i) \neq (S_i')^{e} \mod N$.

Briefly verification process is shown as follows:

\[(S_1' \times S_2' \times S_3')^{e} = [h(M_1)^a \times h(M_2)^a \times h(M_3)^a] \mod N \]
\[= ([h(M_2)^a \times h(M_3)^a \times h(M_1)^a] \mod N \]
\[= h(M_1)^a \times h(M_2)^a \times h(M_3)^a \mod N. \]

In the second attack, we multiplied the signature factor by the random factor and made the multiplication factor be one. If this is the case, it will satisfy Equation (1) in Harn’s scheme. First, we assume Alice sends the messages $(M_1, S_1')$, $(M_2, S_2')$, and $(M_3, S_3')$ to receiver Bob, where $S_i' = a_i \times S_i$, $i = 1, 2, · · · , t$ and $\prod_{i=1}^{t} a_i = 1$. According to the equation shown as follows: $(\prod_{i=1}^{t} S_i)^{e} = \prod_{i=1}^{t} h(M_i) \mod N$, Alice could maliciously make the false signature to Bob and he still believes the signature is generated from Alice. The following is a simple example to illustrate the second attack.

First, we assume that Alice is a dishonest signer who forges the messages $S_1'$, $S_2'$, and $S_3'$, where $S_1' = 1/2 S_1$, $S_2' = 1/4 S_2$ and $S_3' = 8 S_3$. Then, Alice sends messages $(M_1, S_1')$, $(M_2, S_2')$ and $(M_3, S_3')$ to Bob and he would verify the messages $M_1$, $M_2$ and $M_3$ by the equation shown as follows: $(\prod_{i=1}^{t} S_i)^{e} = \prod_{i=1}^{t} h(M_i) \mod N$. Similarly, these fake signatures will pass the verification and Alice could claim that she didn’t transmit the messages to Bob by using this equation, $h(M_i) \neq (S_i')^{e} \mod N$. The completely verification phase is shown as follows:

\[(S_1' \times S_2' \times S_3')^{e} = (1/2 S_1 \times 1/4 S_2 \times 8 S_3)^{e} \mod N \]
\[= (S_1 \times S_2 \times S_3)^{e} \mod N \]
\[= [h(M_1)^a \times h(M_2)^a \times h(M_3)^a] \mod N \]
\[= (h(M_1)^a \times h(M_2)^a \times h(M_3)^a) \mod N \]
\[= h(M_1)^a \times h(M_2)^a \times h(M_3)^a \mod N. \]

2.3. The Restrictions of Harn’s Scheme. In PKI mechanism, the signer utilized his/her private key to sign the message, and then transmits the digital signature to the receiver. Next, the verifier received the digital signature by using signer’s public key. So, the verifier could detect the illegal signature if the verification fails. However, in Harn’s scheme, the verifier could not detect where the signature-verification fault was located if the batch verification fails. Therefore, it is inefficient that the verifier must re-verify each of the signatures and then confirms where the signature-verification fault is located.

According to the weaknesses of Harn’s scheme, in Section 3, we will propose an improved scheme to resist Hwang et al.’s two attacks [8, 9]. Furthermore, our scheme could also detect where the signature-verification fault is located when the verification fails without re-verifying each of these signatures.

3. The Proposed Scheme. Now we present our improved scheme. First of all, when the verifier received the messages $(M_1, S_1)$, $(M_2, S_2)$, · · · , $(M_t, S_t)$ from the signer, the verifier will generate an $m \times n$ matrix(where $m \times n \geq t$) and $t$ random numbers $r_i$, $i = 1, 2, · · · , t$, where $r_i \in \{1, 2, · · · , t\}$ and $r_i$ is one-to-one mapping relation. Table 1 is an example of $m \times n$-matrix. Next, the verifier will randomly fill these $t$ messages in the $m \times n$-matrix position by using the following equation:

\[
\begin{cases}
S(m, n) = S([r_i/n], n), & \text{if } r_i \mod n = 0; \\
S(m, n) = S([r_i/n], r_i \mod n), & \text{otherwise}.
\end{cases}
\]
After filling these messages \((M_1, S_1), (M_2, S_2), \ldots, (M_t, S_t)\) in the \(m \times n\)-matrix, the verifier could batch verify each of the rows and the columns, respectively. The complete batch verifying process is divided into two verifications: row verification and column verification. Briefly steps of row/column verification are shown as follows.

1. **Row Verification:**
   First row: \(\prod_{i=1}^{n} S_{(1,i)} e \equiv \prod_{i=1}^{n} h(M_{1,i}) \mod N\)
   Second row: \(\prod_{i=1}^{n} S_{(2,i)} e \equiv \prod_{i=1}^{n} h(M_{2,i}) \mod N\)
   
   \(\vdots\)
   
   \((m-1)\)th row: \(\prod_{i=1}^{n} S_{(m-1,i)} e \equiv \prod_{i=1}^{n} h(M_{(m-1,i)}) \mod N\)
   
   \(m\)th row: \(\prod_{i=1}^{n} S_{(m,i)} e \equiv \prod_{i=1}^{n} h(M_{(m,i)}) \mod N\)

2. **Column Verification:**
   First column: \(\prod_{i=1}^{n} S_{(1,i)} e \equiv \prod_{i=1}^{n} h(M_{(1,i)}) \mod N\)
   Second column: \(\prod_{i=1}^{n} S_{(2,i)} e \equiv \prod_{i=1}^{n} h(M_{(2,i)}) \mod N\)
   
   \(\vdots\)
   
   \((n-1)\)th column: \(\prod_{i=1}^{n} S_{(n-1,i)} e \equiv \prod_{i=1}^{n} h(M_{(i,n-1)}) \mod N\)
   
   \(n\)th column: \(\prod_{i=1}^{n} S_{(n,i)} e \equiv \prod_{i=1}^{n} h(M_{(i,n)}) \mod N\)

If there are some signature-verification faults in the matrix, we could find out where these signature-verification faults are located by finding the matrix positions of row and column overlaps. We give a simple example to briefly illustrate our scheme as follows.

Suppose Alice sends 25 messages to Bob, then Bob will generate 25 random numbers and a 5 \(\times\) 5 matrix shown as Table 2. If \(r_1 = 16\), the message \((M_1, S_1)\) would be filling in the position \(S(4,1)\) of matrix by using Equation (2). Similarly, if \(r_2 = 3\), the message \((M_2, S_2)\) would be filling in the position \(S(1,3)\) of matrix by using Equation (2), and the rest can be deduced by the similar way. After filling 25 messages in the matrix, Bob could batch verify each of the rows and columns, respectively. As mentioned above, the batch verifying process is divided into row/column verifications and does the following.

- **Row Verification:**
  First row: \(\prod_{i=1}^{n} S_{(1,i)} e \equiv \prod_{i=1}^{n} h(M_{1,i}) \mod N\)
  Second row: \(\prod_{i=1}^{n} S_{(2,i)} e \equiv \prod_{i=1}^{n} h(M_{2,i}) \mod N\)
  Third row: \(\prod_{i=1}^{n} S_{(3,i)} e \equiv \prod_{i=1}^{n} h(M_{3,i}) \mod N\)
  Fourth row: \(\prod_{i=1}^{n} S_{(4,i)} e \equiv \prod_{i=1}^{n} h(M_{4,i}) \mod N\)
  Fifth row: \(\prod_{i=1}^{n} S_{(5,i)} e \equiv \prod_{i=1}^{n} h(M_{5,i}) \mod N\)

- **Column Verification:**
  First column: \(\prod_{i=1}^{n} S_{(1,i)} e \equiv \prod_{i=1}^{n} h(M_{(1,i)}) \mod N\)
  Second column: \(\prod_{i=1}^{n} S_{(2,i)} e \equiv \prod_{i=1}^{n} h(M_{(2,i)}) \mod N\)
  Third column: \(\prod_{i=1}^{n} S_{(3,i)} e \equiv \prod_{i=1}^{n} h(M_{(3,i)}) \mod N\)
  Fourth column: \(\prod_{i=1}^{n} S_{(4,i)} e \equiv \prod_{i=1}^{n} h(M_{(4,i)}) \mod N\)
  Fifth column: \(\prod_{i=1}^{n} S_{(5,i)} e \equiv \prod_{i=1}^{n} h(M_{(5,i)}) \mod N\)

After batch verifying each of the rows and the columns, Bob is now confirmed that the signature-verification fault is really occurring or not. Suppose there was one signature-verification fault in the

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**Table 1. An \(m \times n\) matrix**

<table>
<thead>
<tr>
<th></th>
<th>(S(1,1))</th>
<th>(S(1,2))</th>
<th>(\cdots)</th>
<th>(S(1,n-1))</th>
<th>(S(1,n))</th>
</tr>
</thead>
<tbody>
<tr>
<td>(S(2,1))</td>
<td>(S(2,2))</td>
<td>(\cdots)</td>
<td>(S(2,n-1))</td>
<td>(S(2,n))</td>
<td></td>
</tr>
<tr>
<td>(\vdots)</td>
<td>(\vdots)</td>
<td>(\cdots)</td>
<td>(\vdots)</td>
<td>(\vdots)</td>
<td>(\vdots)</td>
</tr>
<tr>
<td>(S(m-1,1))</td>
<td>(S(m-1,2))</td>
<td>(\cdots)</td>
<td>(S(m-1,n-1))</td>
<td>(S(m-1,n))</td>
<td></td>
</tr>
<tr>
<td>(S(m,1))</td>
<td>(S(m,2))</td>
<td>(\cdots)</td>
<td>(S(m,n-1))</td>
<td>(S(m,n))</td>
<td></td>
</tr>
</tbody>
</table>
Table 2. A $5 \times 5$ matrix

<table>
<thead>
<tr>
<th>S(1,1)</th>
<th>S(1,2)</th>
<th>S(1,3)</th>
<th>S(1,4)</th>
<th>S(1,5)</th>
</tr>
</thead>
<tbody>
<tr>
<td>S(2,1)</td>
<td>S(2,2)</td>
<td>S(2,3)</td>
<td>S(2,4)</td>
<td>S(2,5)</td>
</tr>
<tr>
<td>S(3,1)</td>
<td>S(3,2)</td>
<td>S(3,3)</td>
<td>S(3,4)</td>
<td>S(3,5)</td>
</tr>
<tr>
<td>S(4,1)</td>
<td>S(4,2)</td>
<td>S(4,3)</td>
<td>S(4,4)</td>
<td>S(4,5)</td>
</tr>
<tr>
<td>S(5,1)</td>
<td>S(5,2)</td>
<td>S(5,3)</td>
<td>S(5,4)</td>
<td>S(5,5)</td>
</tr>
</tbody>
</table>

Table 3. The size of Row and Column values chosen with 25 messages in our scheme

<table>
<thead>
<tr>
<th>Sizes of Row</th>
<th>Sizes of Column</th>
<th>computation time</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>13</td>
<td>1.752</td>
</tr>
<tr>
<td>3</td>
<td>9</td>
<td>1.372</td>
</tr>
<tr>
<td>4</td>
<td>7</td>
<td>1.292</td>
</tr>
<tr>
<td>5</td>
<td>5</td>
<td>1.092</td>
</tr>
</tbody>
</table>

position $S(3,3)$ of matrix, Bob could easily realize there was a signature-verification fault occurring and precisely detects where the signature-verification fault is located. From the row/column verifications of the mentioned above, there would occur two verification fails and these two fails would occur in the third row and the third column, respectively. According to the verification fails of the third row and the third column overlaps, the signature-verification fault could be precisely detected in the position $S(3,3)$ of matrix. Therefore, our scheme could resist the first attack proposed by Hwang et al. In addition, our scheme filled the messages in $m \times n$ matrix at random that it could prevent the second attack proposed by Hwang et al. [8, 9].

4. Implementation and Result Analysis.

4.1. Experiment Results. In order to clarify the proposed scheme, in this section, we implement the following three schemes: RSA, Harn’s scheme, and ours. In our proposed scheme, the size of row and column values chosen is shown in Table 3. In addition, the performance results of RSA, Harn’s scheme, and ours are shown in Table 4 in terms of computation time. From Table 3 shows, the performance of our scheme is related to the numbers of row and column selection. When this situation is achieved that both of the numbers of row and column square root are equal to the messages numbers, the performance of our scheme would be the best.

From Table 4 shows, the performance of Harn’s scheme is better when the batch verifying multiple signatures has succeeded. Contrary to the previous situation, if there are signature-verification faults in Harn’s scheme, it must re-verify all of these multiple signatures like RSA. Thus, the performance of Harn’s scheme will be worse in comparison with RSA and our schemes. Although the performance of our scheme isn’t the best, yet the signature-verification faults occur, the performance of RSA and Harn’s schemes will far behind our scheme in terms of required computational time.

In comparison with required computation-time of batch multiple signatures verification of RSA and ours shown in Figure 1, the performance of our scheme is not always suitable for the batch multiple signatures verification, particularly less messages amount. According to the experiment result shows, in Figure 1, the performance of RSA batch verification is close to our scheme when the messages amount is 4.

4.2. Analysis of Illegal Signatures Detection. From the proposed scheme mentioned in the previous section, the performance of illegal signatures detection is regarded as the position it is located in. The best condition is only one illegal signature is detected and we could detect where the signature-verification fault is located immediately. If not (two illegal signatures and upward are detected), the detection results
### Table 4. Experiment results of RSA, Harn and our schemes

<table>
<thead>
<tr>
<th>Method</th>
<th>Messages</th>
<th>Time</th>
<th>Sizes of Row</th>
<th>Sizes of Column</th>
</tr>
</thead>
<tbody>
<tr>
<td>RSA</td>
<td>25</td>
<td>2.884</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Harn</td>
<td>25</td>
<td>0.34</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Ours</td>
<td>25</td>
<td>1.092</td>
<td>5</td>
<td>5</td>
</tr>
<tr>
<td>RSA</td>
<td>100</td>
<td>12.178</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Harn</td>
<td>100</td>
<td>1.031</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Ours</td>
<td>100</td>
<td>2.283</td>
<td>10</td>
<td>10</td>
</tr>
<tr>
<td>RSA</td>
<td>256</td>
<td>30.344</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Harn</td>
<td>256</td>
<td>2.304</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Ours</td>
<td>256</td>
<td>3.535</td>
<td>16</td>
<td>16</td>
</tr>
</tbody>
</table>

**Figure 1.** Comparison with RSA and our schemes

is regarded as the position it is located in. In the following, we demonstrated two conditions included: one illegal signature is detected and two illegal signatures are detected.

1. One Illegal Signature:
   From Figure 2 shows, the batch verification failure is occurring on the third row and third column. As a result, from the position of the overlapping of third row and third column, it confirms that the signature $S(3, 3)$ is illegal and the number of total verification times is 10(5 rows and 5 columns).

2. Two Illegal Signatures:
   At this situation, there are three consequences from the detection: two of the signature-verification faults are occurring on the same row or on the same column; two signature-verification faults are occurring on adjacent diagonal; two signature-verification faults are not occurring on the same row or not on the same column. Brief explanations are described as follows.
   - Two signature-verification faults are occurring on the same row or on the same column.
     In Figure 3, it could confirm that both the two signatures $S(3, 3)$ and $S(3, 4)$ are illegal immediately because the batch verification failures are occurring on the third row, third column and fourth column. So in this case, the number of total verification times is 10(5 rows and 5 columns).
   - Two signature-verification faults are occurring on adjacent diagonal.
     In Figure 4, if the failed batch verifications are occurring on the second row, third row, third column and fourth column. It must verify both the four signatures $S(2, 3), S(3, 3), S(2, 4)$ and
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<table>
<thead>
<tr>
<th>Column 1</th>
<th>Column 2</th>
<th>Column 3</th>
<th>Column 4</th>
<th>Column 5</th>
</tr>
</thead>
<tbody>
<tr>
<td>Row 1</td>
<td>S(1, 1)</td>
<td>S(1, 2)</td>
<td>S(1, 3)</td>
<td>S(1, 4)</td>
</tr>
<tr>
<td>Row 2</td>
<td>S(2, 1)</td>
<td>S(2, 2)</td>
<td>S(2, 3)</td>
<td>S(2, 4)</td>
</tr>
<tr>
<td>Row 3</td>
<td>S(3, 1)</td>
<td>S(3, 2)</td>
<td>S(3, 3)</td>
<td>S(3, 4)</td>
</tr>
<tr>
<td>Row 4</td>
<td>S(4, 1)</td>
<td>S(4, 2)</td>
<td>S(4, 3)</td>
<td>S(4, 4)</td>
</tr>
<tr>
<td>Row 5</td>
<td>S(5, 1)</td>
<td>S(5, 2)</td>
<td>S(5, 3)</td>
<td>S(5, 4)</td>
</tr>
</tbody>
</table>

Figure 2. One illegal signature

<table>
<thead>
<tr>
<th>Column 1</th>
<th>Column 2</th>
<th>Column 3</th>
<th>Column 4</th>
<th>Column 5</th>
</tr>
</thead>
<tbody>
<tr>
<td>Row 1</td>
<td>S(1, 1)</td>
<td>S(1, 2)</td>
<td>S(1, 3)</td>
<td>S(1, 4)</td>
</tr>
<tr>
<td>Row 2</td>
<td>S(2, 1)</td>
<td>S(2, 2)</td>
<td>S(2, 3)</td>
<td>S(2, 4)</td>
</tr>
<tr>
<td>Row 3</td>
<td>S(3, 1)</td>
<td>S(3, 2)</td>
<td>S(3, 3)</td>
<td>S(3, 4)</td>
</tr>
<tr>
<td>Row 4</td>
<td>S(4, 1)</td>
<td>S(4, 2)</td>
<td>S(4, 3)</td>
<td>S(4, 4)</td>
</tr>
<tr>
<td>Row 5</td>
<td>S(5, 1)</td>
<td>S(5, 2)</td>
<td>S(5, 3)</td>
<td>S(5, 4)</td>
</tr>
</tbody>
</table>

Figure 3. Two illegal signatures are occurring on the same row

S(3, 4) to confirm where the two faults are located. Finally, it confirms that the two signatures S(3, 3) and S(2, 4) are illegal and the number of total verification times is 14 (5 rows, 5 columns and 4 signatures).

- Two signature-verification faults are not occurring on the same row or not on the same column. In Figure 5, the batch verification failures are occurring on the first row, third row, third column and fourth column. It must verify both the four signatures S(1, 3), S(3, 3), S(3, 4) and S(1, 4) to confirm where the two faults are located. Thus, it confirms that the two signatures S(3, 3) and S(1, 4) are illegal and the number of total verification times is 14 (5 rows, 5 columns, and 4 signatures).
In the above mentioned situations, the worst case of detecting the illegal signatures is that the two illegal signatures are occurring on adjacent diagonal and the two illegal signatures are not occurring on the same row or not on the same column. Both the number of total verification times of these two situations are 14. Although in this worst case, our scheme is still more efficient than Harn’s scheme because their scheme must re-verify all individual signatures separately to confirm where the illegal signatures are located and the number of total verification times is 26 (25 messages and 1 batch verification).

5. Conclusions. In this paper, we have introduced three authentication methods including RSA, Harn’s, and our improved scheme. We also introduce the performance of these three schemes and some illegal signature detections in our scheme. According to the experiment result shows, the performance of Harn’s
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A batch verifying and detecting the illegal signatures scheme is the best when there does not have any signature-verification faults and our scheme is not suitable for batch verification when the messages amount is small. However, there are two kinds of attacks that can cheat Harn’s scheme and their scheme cannot find out where the signature-verification fault is located if the batch verification fails. Therefore, our improved scheme could resist the weakness of Harn’s and quickly find out where the signature-verification fault is located.

REFERENCES


